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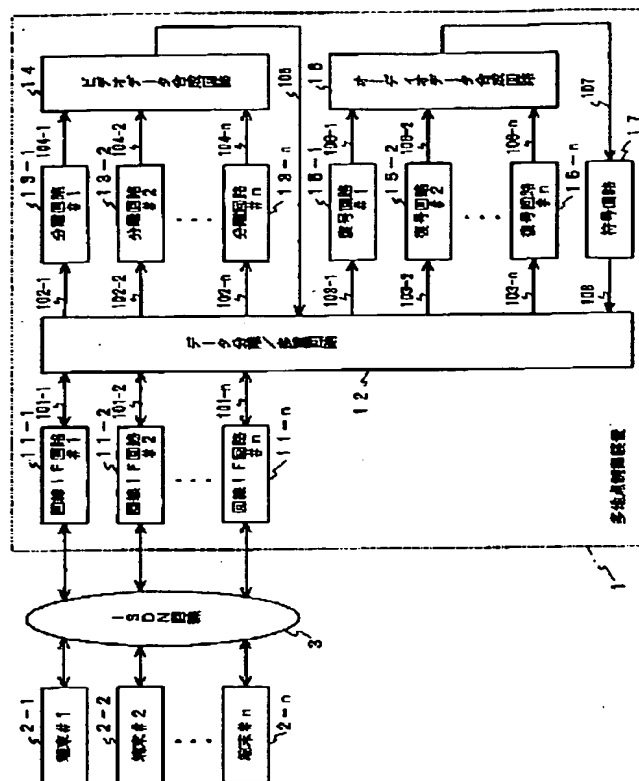PUBLICATION DATE : 16-03-01

APPLICATION DATE : 25-08-99
APPLICATION NUMBER : 11237694

APPLICANT : NEC CORP;

INVENTOR : TANOI HISAYUKI;

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TITLE : MULTI-POINT CONTROLLER AND VIDEO DISPLAY METHOD USED FOR IT

ABSTRACT : PROBLEM TO BE SOLVED: To provide a multi-point controller that can make simple image synthesis display in spit of a small circuit scale without the need for H.261 video data decoding.

SOLUTION: An H.221 data demultiplexer/multiplexer circuit 12 of this multi- point controller detects an H.221 frame synchronizing signal from H.221 received data and demulitiplexes video data and audio data. After H.261 video data demultiplexer circuits 13-1-13-n apply error correction decoding to the video data, they detect only an intra-frame subjected to in-frame coding, demultiplexes QCIF video data by one frame and output the video data. An H.261 video data synthesis circuit 14 synthesizes video data by one frame into one CIF frame, add various headers to the CIF frame again, applies again error correction coding to the resulting frame and outputs the H.261 video data.

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